

Application Data Sheet**Application Information**

Application Type::	Regular
Subject Matter::	Utility
CD-ROM or CD-R:	None
Title::	TRANSISTOR, INTEGRATED CIRCUIT, ELECTRO-OPTIC DEVICE, ELECTRONIC INSTRUMENT AND METHOD OF MANUFACTURING A TRANSISTOR
Attorney Docket Number::	117849
Total Drawing Sheets::	10
Small Entity::	No

Applicant Information

Applicant Authority type::	Inventor
Primary Citizenship Country::	Japan
Status::	Full Capacity
Given Name::	Ichio
Family Name::	YUDASAKA
City of Residence::	Suwa-shi
State or Province of Residence::	Nagano-ken
Country of Residence::	Japan

Applicant Authority type::	Inventor
Primary Citizenship Country::	Japan
Status::	Full Capacity
Given Name::	Masahiro
Family Name::	FURUSAWA
City of Residence::	Suwa-shi
State or Province of Residence::	Nagano-ken
Country of Residence::	Japan

Applicant Authority type::	Inventor
Primary Citizenship Country::	Japan
Status::	Full Capacity
Given Name::	Takashi
Family Name::	AOKI
City of Residence::	Suwa-shi
State or Province of Residence::	Nagano-ken
Country of Residence::	Japan

Correspondence Information

Correspondence Customer Number:: 25944

Foreign Priority Information			
Country::	Application Number::	Filing Date::	Priority Claimed::
Japan	2002-351112	12/03/2002	Yes
Assignee Information			
Assignee Name::		Seiko Epson	
Street of mailing address::		4-1, Nishishinjuku 2-chome, Shinjuku-ku	
City of mailing address::		Tokyo	
Country of mailing address::		Japan	